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Applicatio	n No./Patent No.: 719	15/Le	Filed/Issue Date: _3/20/07						
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states that	- •	(Type of	Assignee, e.g., corporation, partnership, university, government agency, etc.						
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2.	an assignee of less than the entire right, title, and interest in (The extent (by percentage) of its ownership interest is%); or								
3.	the assignee of an undivided interest in the entirety of (a complete assignment from one of the joint inventors was made)								
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The under	signed (whose title is supplied	ed below) is authorized to act on	behalf of the assignee.						
	gnature	y on	3-16-2009						
()SI	ynawie .	A 4	Date						
	Jessica L	. Olson	Attorney						
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Patent Assignment Abstract of Title

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Patent #: 7191516

Issue Dt: 03/20/2007

Application #: 10621844

Filing Dt: 07/16/2003

Publication #: 20050011656

Pub Dt: 01/20/2005 Inventor: Janet Patterson

Title: METHOD FOR SHIELDING INTEGRATED CIRCUIT DEVICES

Assignment: 1

Reel/Frame: 014310/0482

Recorded: 07/16/2003

Pages: 2

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

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Exec Dt: 07/10/2003

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Name	Emily Snyder	/			Telephone +1	(858) 503-3341				
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